

FIG. 1

FIG. 1 is a block diagram of a system 100 in accordance with the present invention. The system 100 includes a host computer 110 and a peripheral device 120. The host computer 110 includes a CPU 111, a Memory 112, and a Comm. Adapter 113. The peripheral device 120 includes a CPU 121, a Memory 122, and a Comm. Adapter 123. The host computer 110 and the peripheral device 120 are connected to each other via a communication bus 114. The peripheral device 120 is also connected to a network 124 via a network interface 125.

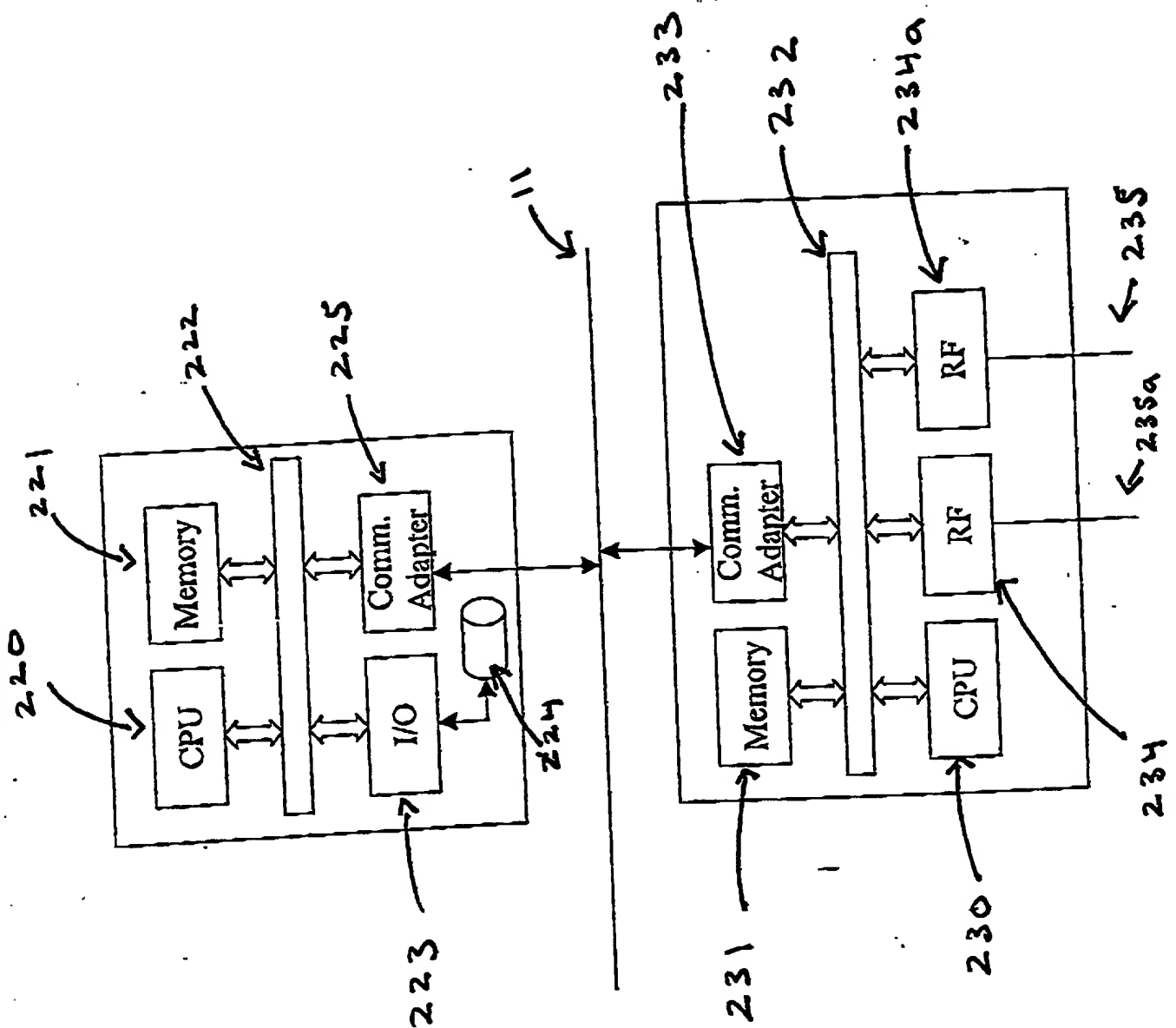


Fig 2

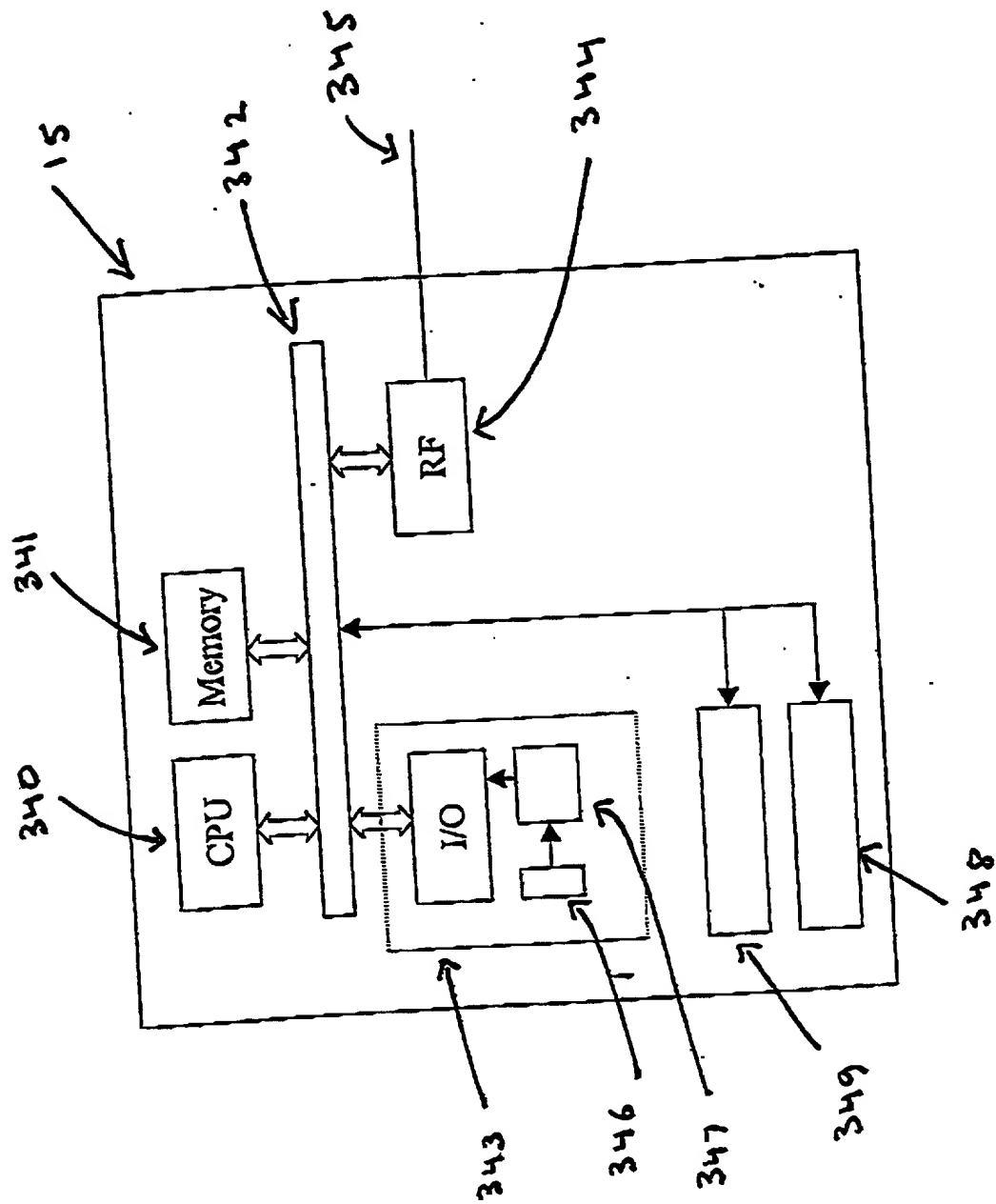


FIG. 3

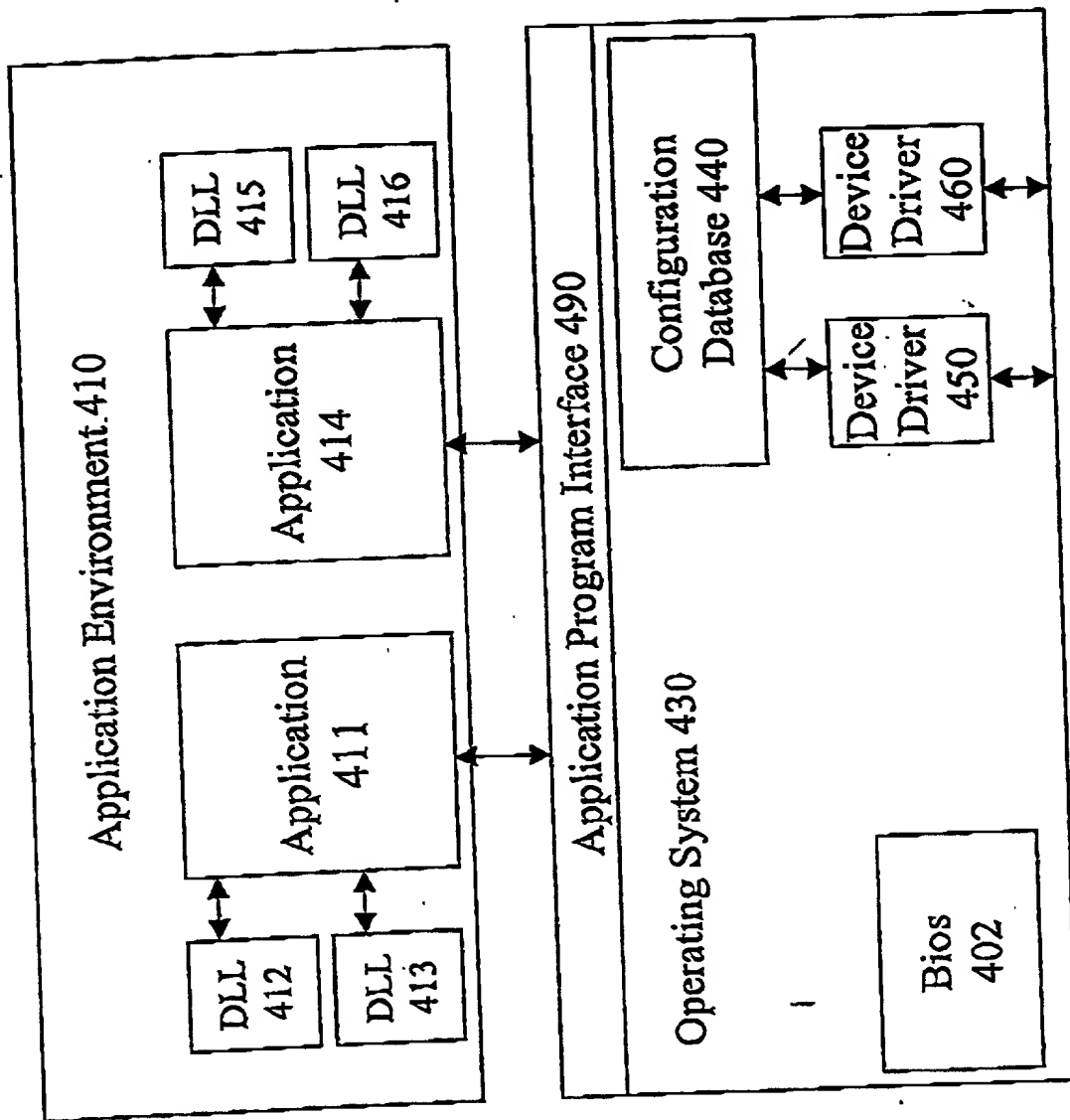


Fig. 4

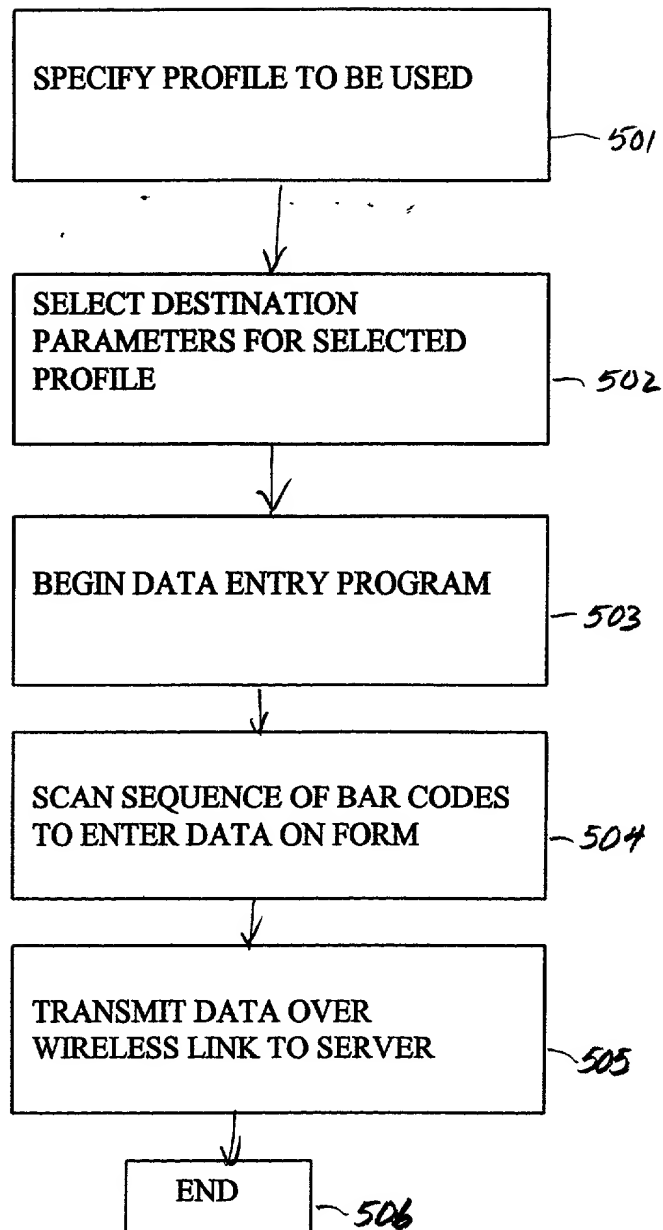


FIG 5

FIG 10

